



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

~~CONFIDENTIAL~~ Patent Application of

ELIYAHOU HARARI, et al.

Serial No.: 08/174,768

Filed: December 29, 1993

For: FLASH EEPROM SYSTEM

Group Art Unit: 2313

RESERVED

JAN 04 1995

GROUP 2300

San Francisco, California

Hon. Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

**CERTIFICATE OF EXPRESS MAILING UNDER 37 CFR 1.10**

I hereby certify that this patent application transmittal and the documents referred to as enclosed therein are being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" Mailing Label Number TB614039217US addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on December 22, 1994.

Julie M. Balot

Signature \_\_\_\_\_

Date \_\_\_\_\_

## PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified patent application, prior to a first Examiner's Action, by cancelling claims 56-62, all of the claims now in the application, and substitute the following new claims therefore:

~~4~~63. A memory system on a card that is connectable to a computer system, comprising:

an array of non-volatile floating gate memory cells partitioned into a plurality of sectors that individually include a distinct group of said array of memory cells that are erasable together as a unit,

means including a memory controller that is connectable

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